

## Dr. Amretashis Sengupta

M.Sc., M.Tech, Ph.D.,  
MInstP, MRSC, SMIEEE, FHWK

SERB Research Scientist  
University of North Bengal  
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Date of Birth: 23/10/1984, Gender:

Male, Nationality: Indian

## Education:

**Ph.D.** (Engg.), Jadavpur University,  
Kolkata, India - Dec 2012.

**M.Tech. (Nano Science & Technology)** Jadavpur University,  
Kolkata, India - **2010 (1st Class 1<sup>st</sup>)**

**M.Sc. (Physics)**, North Bengal  
University, WB, India - **2007 (1st Class 1<sup>st</sup>)**

**B.Sc. (Physics) (Hons.)** North  
Bengal University, WB, India - **2005 (1st Class 2<sup>nd</sup>)**

CSIR-UGC NET qualified **2008**

GATE qualified **2007**

## Publications Summary

International Journals: **32** Books: **1**

Conferences: **33** Invited Talks: **9**

**Total Citations: 431**

**h-index: 12 i-10 index: 14**

Google Scholar Link:



Scopus Author ID: 36538068500

Researcher ID: L-9785-2014

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## Post-Doctoral Research experience

Research Associate (Grade-7) in **James Watt School of Engineering, University of Glasgow, UK.** (July 2019 – Jan 2020).

Visiting Researcher in DFG Research Training Group in Quantum Mechanical Materials (QM3) Modeling at **Bremen Center for Computational Materials Science (BCCMS), University of Bremen, Germany.** (Oct 2017 – Jan 2018).

Hanse-Wissenschaftskolleg (HWK) Fellow in Energy Research, **Bremen Center for Computational Materials Science (BCCMS), University of Bremen, Germany.** (Aug 2016 – June 2017).

Visiting Researcher in **Department of Physics and Earth Science, Jacobs University, Bremen, Germany.** (July - Sep 2015).

Visiting Researcher in **Institute-I, Theoretical Physics, Universität Regensburg, Germany.** (June – July 2015).

DST Post-doc Fellow in Nano Science and Technology, CEDT, **Indian Institute of Science, Bangalore.** (Dec 2012 - Mar 2014).

## Skill-set:

Experience in the field of physics, materials science and nanotechnology inclusive of both computational (atomistic modelling- DFT, DFTB, and multi-scale modelling) and experimental skills (nanostructured materials fabrication, thin films, sensors and advanced characterization techniques).

## Teaching Experience:

**Assistant Professor** at IEST Shibpur April 2014- March 2019. (**5years**)

**Course Taught:** Semiconductor Physics and Devices, VLSI Technology

Research Associate (Grade-7) in **University of Glasgow, UK.** (6 months)

**Course Taught:** Materials-1 (ENG). -Teaching Assistantship

**Indian Institute of Science** (2013-14)-Teaching Assistantship

**Jadavpur University** (2011-12)-Teaching Assistantship

**Ph.D. thesis Supervised – 3, M.Tech. Thesis supervised – 6**

**Total experience** (post Ph.D. teaching and research) **8 yrs. 8 months+**

## Awards and Fellowships Received:

**Hanse-Wissenschaftskolleg (HWK) Fellowship (Germany)** (2016-17).

Visiting Researcher in **DFG Research Training Group-QM3** at University of Bremen.

Visiting Researcher under **DFG Aufbau International Cooperation program** (2015).

**SERB Research Scientist fellowship** awarded by Science and Engineering Research Board (SERB), Govt. of India. (2020)

**DST INSPIRE Faculty Award**, from Department of Science & Technology, Govt. of India.(2013).

Awarded **DST Postdoctoral Fellowship in Nano Science & Technology** (2012).

Awarded **DST INSPIRE fellowship.** (2010)

**University Gold Medal** for securing first class first position in M.Tech. (Nano Science & Technology). (2010)

Awarded **University Medal and two endowment medals** for securing first class first position in M.Sc. (Physics) (2007).

**University Medal** for securing first class second position in B.Sc.(Hons.) (2005).

## Professional Recognitions:

Reviewer of over 15 scientific journals including ACS Nano, Appl. Surf. Sci., Adv. Energy Mater., Appl. Phys. Lett., J. Appl. Phys.& Proc. Royal Soc. London A.

Member of German Physical Society (DPG), Institute of Physics (IOP), Royal Society of Chemistry (RSC), Senior Member of IEEE and IEEE Nanotechnology Council, Life member of MRSI and Indian Physical Society.

Chair, India Chapter, University of Bremen Alumni Association.

## Language Skills:

English (IELTS 8.0, CEFR C1), German (CEFR A2), Bengali, Hindi.

## Books:

A. Sengupta and C.K. Sarkar (Eds.), “Introduction to Nano: Basics to Nanoscience and Nanotechnology”, Springer Verlag (Berlin/Heidelberg), 2015. (ISBN: 978-3-662-47313-9)

## International Journals

1. A. Sengupta, ‘Electronic and optical properties of SnX<sub>2</sub>(X=S, Se)-InSe van der Waals heterostructures from first-principle calculations’, **Physica Scripta**, vol. 94, no. 12, pp. 125806 (1-7) (2019). (Impact Factor : 2.151).
2. A. Sengupta, A. Dominguez, and T. Frauenheim, ‘Photo-absorption properties of van der Waals heterostructure of monolayer InSe with silicene, germanene and antimonene’, **Applied Surface Science**, vol. 475, pp. 774-780 (2019). (Impact Factor : 6.182).
3. A. Sengupta, ‘Lithium and Sodium adsorption properties of two-dimensional aluminum nitride’, **Applied Surface Science**, vol. 451, pp. 141-147 (2018). (Impact Factor : 6.182).
4. B. Sharma, A. Sengupta and C.K. Sarkar, ‘Computational study of CNT based Nanoscale Reversible Mass Transport Archival Memory with Fe, Co and Ni nano-Shuttles’, **Computational Materials Science**, vol. 146, pp. 112-118 (2018). (Impact Factor : 2.644).
5. L. Banerjee, A. Sengupta, H. Rahaman, ‘Carrier transport and thermoelectric properties of differently shaped Germanene (Ge) and Silicene (Si) nanoribbon interconnects,’ **IEEE Transactions on Electron Devices**, vol. 66, Iss. 1, pp. 664-669 (2018) (Impact Factor : 2.704).
6. B. Sharma, A. Mukhopadhyay, L. Banerjee, A. Sengupta, H. Rahaman, C. K. Sarkar, ‘Ab initio study of mono-layer 2-D insulators (X-(OH)<sub>2</sub> and h-BN) and their use in MTJ Memory device’, **Microsystem Technologies**, vol. 25, Iss. 5, pp. 1909-1917 (2019). (Impact Factor : 1.513).
7. A. Sengupta and T. Frauenheim, ‘Lithium and Sodium adsorption properties of monolayer antimonene’, **Materials Today Energy**, vol. 5, pp. 347-354 (2017). (Impact Factor: 5.604)
8. A. Sengupta, ‘Atomistic study of electrostatics and carrier transport properties of CNT@MS<sub>2</sub> (M= Mo,W) and CNT@BN core-shell nanotubes’, **Journal of Materials Science**, vol. 52, No. 13, pp. 8119-8131 (2017). (Impact Factor : 2.302).
9. A. Sengupta, ‘On the junction physics of Schottky contact of (10, 10) MX<sub>2</sub> (MoS<sub>2</sub>, WS<sub>2</sub>) nanotube and (10, 10) carbon nanotube (CNT): an atomistic study’, **Applied Physics A**, vol. 123, pp. 227 (1-9) (2017) (Impact Factor : 1.517).
10. A. Sengupta, M. Audiffred, T. Heine, T.A. Niehaus, ‘Stacking dependence of carrier transport properties in multilayered black phosphorous’, **Journal of Physics: Condensed Matter**, vol. 28, pp. 075001 (1-10) (2016). (Impact Factor : 2.346)
11. L. Banerjee, A. Mukhopadhyay, A. Sengupta, H. Rahaman, ‘Performance analysis of uniaxially strained monolayer black phosphorus- and blue phosphorus-based n-MOSFET and p-MOSFET’, **Journal of Computational Electronics**, vol. 15, Iss. 3, pp. 919-930 (2016). (Impact Factor : 1.520)
12. B. Sharma, A. Mukhopadhyay, A. Sengupta, H. Rahaman, C. K. Sarkar, ‘Analysis of tunneling currents in Multilayer Black Phosphorous and MoS<sub>2</sub> non-volatile flash memory cells’, **Journal of Computational Electronics**, Vol. 15, Issue 1, pp. 129-137 (2016). (Impact Factor : 1.520)
13. A. Mukhopadhyay, L. Banerjee, A. Sengupta, H. Rahaman ‘Effect of Stacking Order on Device Performance of Bilayer Black Phosphorene-FET’ **Journal of Applied Physics**, vol. 118, pp. 224501 (1-5) (2015). (Impact Factor : 2.276)
14. A. Sengupta, D. Saha, T.A. Niehaus and S. Mahapatra, ‘Effect of line defects on the electrical transport properties of monolayer MoS<sub>2</sub> sheet’, **IEEE Transactions on Nanotechnology** vol. 14, No. 1, pp. 51-56 (2015). (Impact Factor : 1.825)
15. A. Sengupta, A. Chanana, and S. Mahapatra, ‘Phonon scattering limited performance of monolayer MoS<sub>2</sub> and WSe<sub>2</sub> n-MOSFET’, **AIP Advances**, vol. 5, No. 2, pp. 027101 (1-9) (2015). (Impact Factor : 1.591)
16. A. Sengupta and C. K. Sarkar, ‘Study on Nanoparticles Embedded Multilayer Gate Dielectric MOS Non Volatile Memory Devices’, **International Journal of Nanotechnology – IEEE INEC Special Issue**, Vol.11, No.12, pp.1073 – 1080 (2014). (Impact Factor : 1.335)
17. D. Saha, A. Sengupta, S. Bhattacharyya and S. Mahapatra, ‘Impact of Stone-Wales and lattice vacancy defects on the electro-thermal transport of the free standing structure of metallic ZGNR’ **Journal of Computational Electronics** Volume 13, Issue 4, pp 862-871 (2014). (Impact Factor : 1.520)
18. A. Chanana, A. Sengupta, and S. Mahapatra, ‘Performance Analysis of Boron Nitride Embedded Armchair Graphene Nanoribbon MOSFET with Stone Wales Defects’ **Journal of Applied Physics** Vol. 115, Issue 3, pp. 034501 (2014) (Impact Factor : 2.276)
19. A. Sengupta and S. Mahapatra, ‘Negative differential resistance and effect of defects and deformations in MoS<sub>2</sub> armchair nanoribbon MOSFET’, **Journal of Applied Physics** Vol. 114, pp. 194513 (2013). (Impact Factor : 2.276)
20. A. Sengupta Ram Krishna Ghosh and S. Mahapatra, ‘Performance Analysis of Strained Monolayer MoS<sub>2</sub> MOSFET’, **IEEE Transactions on Electron Devices** Vol. 60, pp. 2782 (2013). (Impact Factor : 2.472)
21. A. Sengupta and S. Mahapatra, ‘Performance limits of transition metal dichalcogenide (MX<sub>2</sub>) nanotube surround gate ballistic field effect transistors’, **Journal of Applied Physics** Vol. 113, pp. 194502 (2013). (Impact Factor : 2.276)
22. A. Sengupta, and C. K. Sarkar, ‘Surface Potential Based Analytical Modeling of Double Gate MOSFET Non-Volatile Memory with Si and Au Nano-dots Embedded Gate Dielectric’, **Journal of Computational & Theoretical Nanoscience**, Vol. 10, No. 4, pp. 906-913 (2013) (Impact Factor : 1.343)
23. A. Sengupta, C. K. Sarkar, and Felix G. Requejo, ‘Semi-Analytical modeling of Ag and Au nanoparticles and fullerene (C<sub>60</sub>) embedded gate oxide Compound Semiconductor MOSFET memory Devices’, **Journal of Computational Electronics**, Vol. 11, Issue 4, pp. 303-314 (2012) (Impact Factor : 1.520)

24. S. Dash, G. Chakraborty, **A. Sengupta**, C. K. Sarkar, 'Optimization of tunneling currents through CNT & Si nanocrystals embedded gate oxide MOS Structure using Genetic Algorithm approach for memory device application', **Journal of Computational and Theoretical Nanoscience**, Vol. 9, No. 3, pp. 434-440 (2012) (Impact Factor : 1.343)
25. **Amretashis Sengupta**, Pashupati Shah, Chandan Kumar Sarkar, Felix G. Requejo, 'Computational study on semiconducting and metallic nanocrystal embedded gate oxide MOS non-volatile memory devices', **Advanced Science Letters**, Vol. 10, No. 1, pp. 47-54 (2012) (Impact Factor : 1.253)
26. **A. Sengupta**, and C. K. Sarkar, 'Analytical Modelling of Si and Au Nanocrystal Embedded Multilayer Gate Dielectric Long Channel Silicon Nanowire Surround Gate MOSFET Non Volatile Memory Devices' **Nanoscience & Nanotechnology Letters**, Vol. 4, No. 7, pp. 667-675 (2012) (Impact Factor : 1.431)
27. **A. Sengupta**, C. K. Sarkar, F. G. Requejo, 'Comparative study of silicon nanowire, CNT and fullerene embedded multilayer high-k gate dielectric MOS memory devices', **Journal of Physics D: Applied Physics** Vol. 44, No. 36, pp. 405101 (2011). (Impact Factor : 2.721)
28. G. Chakraborty, **A. Sengupta**, F.G.Requejo, C.K. Sarkar, 'Study of the relative performance of Silicon and Germanium nanoparticles embedded gate oxide in metal-oxide-semiconductor memory devices' **Journal of Applied Physics**. Vol. 109, pp. 064504 (2011). (Impact Factor : 2.276)
29. **A. Sengupta**, S. Maji and H. Saha, 'CBD Grown aligned ZnO nanorods based methane sensor and the effect of Pd sensitization', **Advanced Science Letters**, Vol. 3, No. 4, pp. 385-392 (2010). (Impact Factor : 1.253)
30. S. Maji, P. Bhattacharyya, **A. Sengupta**, and H. Saha, 'Growth and Characterization of Nano-cups, Flowers and Nanorods of ZnO by Chemical Bath Deposition', **Advanced Science Letters**, Vol. 3, No. 2, pp. 154-160 (2010). (Impact Factor : 1.253)
31. G. P. Mishra, **A. Sengupta**, S. Maji, S. K. Sarkar and P. Bhattacharyya, 'The Effect of Catalytic Metal Contact on Methane Sensing Performance of Nanoporous ZnO -Si Heterojunction', **International Journal on Smart Sensing and Intelligent Systems**, Vol. 3, No. 2, pp. 273-291 (2010).
32. P. Bhattacharyya, S. Maji, S. Biswas, **A. Sengupta**, T. Maji, H. Saha, 'Palladium Surface Modification of Nanocrystalline Sol-Gel derived Zinc Oxide Thin Films and its Effect on Methane Sensing', **Sensors & Transducers Journal**, Vol. 110, Issue 11, pp.38-46, (2009)

### International Conference Publications

1. **A. Sengupta**, "Layered CrO<sub>2</sub> as an electrode material candidate for supercapacitor applications", **AIP Horizons: Energy Storage and Conversion, August 4-6, 2021, USA.**
2. **A. Sengupta**, "Li adsorption properties of monolayer Cu sheets", **ACS Spring meeting 2021, April 5-30, 2021, USA.**
3. **A. Sengupta**, "2 dimensional GaS as a potential electrode material for supercapacitor applications", **23rd IOP Interdisciplinary Surface Science Conference (ISSC-23), 19-21 April, London, UK.**
4. **A. Sengupta**, "Enhancement of Evanescent Bloch Waves in monolayer 1T'-ReS<sub>2</sub> under Compressive Strain" **Chemistry of 2-dimensional materials: beyond graphene Faraday Discussion, 23-25 November, 2020, Royal Society of Chemistry, London, UK.**
5. **A. Sengupta**, "Thermoelectric Properties of Patterned Hybrid Zigzag Siligene Nanoribbons" **Chemistry of 2-dimensional materials: beyond graphene Faraday Discussion, 23-25 November, 2020, Royal Society of Chemistry, London, UK.**
6. **A. Sengupta**, "Electronic and Optical properties of 1T'-ReS<sub>2</sub> Graphene van der Waals heterostructures", **2020 IEEE International Conference Nanomaterials: Applications & Properties (IEEE NAP-2020), 9-13 November, 2020, Sumy, Ukraine.**
7. **A. Sengupta**, "Magnesium Adsorption Properties of Two Dimensional Silicon Germanium Hybrid for Electrode Application in Ion Batteries", **2nd Battery and Energy Storage Conference, 21-23 October, 2020, American Institute of Chemical Engineers, New York, USA.**
8. V. P. Georgiev, **A. Sengupta**, P. Maciazek, O. Badami, C. Medina-Bailon, T. Dutta, F. Adamu-Lema, A. Asenov, "Simulation of gated GaAs-AlGaAs resonant tunneling diodes for tunable terahertz communication applications", **25th IEEE International Conference on Simulation of Semiconductor Processes and Devices (SISPAD 2020), September 23 - October 6, 2020, Japan.**
9. **A. Sengupta**, "On the adatom doping of 2-Dimensional siligene (SiGe) with alkali metals", **DPG Spring Meeting-2018, 11-16 March, 2018, Berlin, Germany.**
10. **A. Sengupta**, "Performance limits of monolayer 1T<sub>ES</sub>-ReS<sub>2</sub> nanoscale MOSFETs", **IEEE Nanotechnology Materials and Devices Conference 2017 (IEEE NMDC 2017), 2-4 October 2017, Singapore.**
11. L. Banerjee, A. Mukhopadhyay, B. Sharma, **A. Sengupta**, H. Rahaman, "Performance analysis of 2D Graphene FET embedded with hexagonal Boron Nitride clusters", **IEEE Devices for Integrated Circuit (DevIC 2017), Kolkata, 23-24 March 2017.**
12. B. Sharma, A. Mukhopadhyay, L. Banerjee, **A. Sengupta**, H. Rahaman and C. K. Sarkar, "Effect of Ca(OH)<sub>2</sub>, hBN and Mg(OH)<sub>2</sub> based insulators as composite oxides in Magnetic Tunnel Junction Memory Device Properties", **IEEE Devices for Integrated Circuit (DevIC 2017), Kolkata, 23-24 March 2017.**
13. A. Mukhopadhyay, L. Banerjee, B. Sharma, **A. Sengupta**, H. Rahaman and C. K. Sarkar, "Computational Study of Silicene-CNT Double Junctions", **IEEE Devices for Integrated Circuit (DevIC 2017), Kolkata, 23-24 March 2017.**
14. **A. Sengupta**, "Atomistic Simulation of Transport Properties of Non-Graphitic Armchair Nanotubes and Effect of Stone-Wales Defects", **21st International Conference on Simulation of Semiconductor Processes and Devices (SISPAD-2016) held in Nurnberg, Germany, Proc. SISPAD, pp. 97-100 (2016).**

15. **A. Sengupta**, "Influence of size and shape of nano-islands of hexagonal boron nitride (hBN) on optical and transport properties of hBN embedded Graphene nanoribbons", **CeCAM Workshop on Computational insight into photo-induced processes at interfaces, October 10 - October 14, 2016, Bremen, Germany.**(2016)
16. **A. Sengupta** and T. Frauenheim, "Tuning of optical properties by layer engineering tin chalcogenide- graphene van der Waals heterostructure", **CeCAM Workshop on Computational insight into photo-induced processes at interfaces, October 10 - October 14, 2016, Bremen, Germany.** (2016)
17. A. Chanana, **A. Sengupta**, S. Mahapatra, 'Analysis of vacancy defects in hybrid graphene-boron nitride armchair nanoribbon based n-MOSFET at ballistic limit', 2015 **International Workshop on Computational Electronics (IWCE), Sep 2, 2015, Purdue University, West Lafayette, IN, USA.**
18. A. Mukhopadhyay, L. Banerjee, **Amretashis Sengupta**, H. Rahaman, 'Strain Modulated Variations in Monolayer Phosphorene n-MOSFET', 2015 **IEEE Conference on Electron Devices and Solid State Circuits (IEEE EDSSC - 2015), June 1-4, 2015, Singapore.**
19. **A. Sengupta**, "Study of next generation 2D and 1D channel material MOSFETs", **CEFIPRA Indo-French Workshop on Emerging Trends in Electron Device Modeling**, 30 March -1 April, 2015, **Indian Institute of Science, Bangalore.**
20. **A. Sengupta**, "Study of next generation 2-D channel material MOSFETs with empirical tight binding – NEGF formalism", **CeCAM Workshop on High performance models for charge transport in large scale materials systems, 5-10 October, 2014, Bremen, Germany .**
21. B. Sharma, G. Chakraborty, **A. Sengupta**, H. Rahaman, and C.K. Sarkar, 'Study of the Tunneling Characteristics of Metal and Semiconductor Nanoparticles Embedded Gate Dielectric in MOS structures', International Conference on Advanced Materials and Energy Technology (ICAMET) 2014 December 17-19, 2014 , IEST Shibpur, Howrah, West Bengal, India.
22. **Amretashis Sengupta**, and Chandan Kumar Sarkar, 'Comparative Study on Nanocrystal Embedded Gate Dielectric and Oxide Nitride Oxide Stack Dielectric GAA MOSFET Non-volatile Memory Devices', IEEE-IAPR-OSA International Conference on Informatics Electronics & Vision (ICIEV- 2012) Dhaka, Bangladesh, 18th ~19th May, 2012.
23. **Amretashis Sengupta**, Kalyan Koley, and Chandan Kumar Sarkar, 'Subthreshold Charge Leakage in Nanoparticle Embedded DG MOSFET Memory Devices an Analytical Study', IEEE International Conference on Devices, Circuits and Systems (IEEE ICDCS' 12) Karunya University, Coimbatore, 15-16th March 2012.
24. **A. Sengupta**, P. Shah, C.K. Sarkar, 'Use of Nanomaterials for Performance Enhancement of MOS Non Volatile Memory Elements', International Conference on Nanoscience, Engineering & Advanced Computing (ICNEAC-2011), Swarnandhra College of Engg. & Tech., Narsapur, AP, July 8-10, 2011.
25. **Amretashis Sengupta**, Chandan K. Sarkar, 'Study on Nanoparticles Embedded Multilayer Gate Dielectric MOS Non Volatile Memory Devices', **The 4th IEEE International Nano Electronics Conference (4th IEEE INEC-2011), 21-24 June 2011, Chang Gung University, Tao Yuan, Taiwan.**
26. **A. Sengupta**, G.P. Mishra, P. Bhattacharyya, H. Saha, S.K. Sarkar, 'Chemical Bath Deposited ZnO nanorod array p-Si heterojunction methane sensor' International Conference on Fundamental & Application of Nano science & Technology (ICFANT-2010), Jadavpur University, Kolkata, Dec 9-11, 2010.
27. G. Chakraborty, **A. Sengupta**, F.G.Requejo, C.K. Sarkar, 'On the comparative performance of MOS non-volatile memory devices with Silicon and Germanium nanoparticles embedded gate oxide' International Conference on Fundamental & Application of Nano science & Technology (ICFANT-2010), Jadavpur University, Kolkata, Dec 9-11, 2010.
28. **A. Sengupta**, S. Maji, P. Bhattacharyya, H. Saha, 'Synthesis, characterization and methane sensing properties of CBD grown aligned zinc oxide nanorods', International Conference on Advanced Nanomaterials & Nanotechnology (ICANN-2009)IIT-Guwahati, Dec 2009.

### National Conference Publications

1. M. Guha, **A. Sengupta**, M. Sahoo and H. Rahaman, 'Effect of defects on performance and signal integrity of multilayer GNR interconnects', INUP Compact modeling workshop, IISc, Bangalore, 22 - 23 August, 2014.
2. A. Mukhopadhyay, L. Banerjee, **A. Sengupta** and H. Rahaman, 'ON current variations in AGNR-FET due to intrinsic rippling', INUP Compact modeling workshop, IISc, Bangalore, 22 - 23 August, 2014.
3. **A. Sengupta**, S. Maji, H. Saha, 'Low temperature CBD grown ZnO nanostructures on Pd catalyst embedded Porous Silicon (PS) and other substrates', Metallix-2010, 26-27 Feb 2010, Jadavpur University Kolkata. (Awarded 2nd prize in Technical Paper Presentation).
4. **A. Sengupta**, 'Graphene: The Rising Star of Materials Science', Metallix-2009, Jadavpur University, Kolkata, Feb 26-27, 2009.
5. **A. Sengupta**, C.K. Sarkar, 'Recent Developments in Nanoparticle Embedded Gate Dielectric MOS Non-Volatile Memories', National Conference On Engineering Education in the new Century (E2NC-2011), Sir J.C. Bose School of Engg., SKF Group of Institutions, Hooghly, Jan 20-21, 2011.

### Invited Talks

1. Invited Speaker "An ab-initio study of 2 dimensional metal (Cu, Ag) - 1T' ReS<sub>2</sub> van der Waals heterostructure", IEEE DevIC 2021, Kalyani Govt. Engg. College, 19-20th May, 2021.
2. Invited Webinar Talk ""An introduction to Nano: the fascinating world of nanotechnology" , Salesian College, Siliguri Campus, 11th Jan 2021.

3. Invited speaker at IEEE International Symposium on Devices, Circuits and Systems (ISDCS 2018), Talk: "Modeling the 2D flatland: new paradigm for nanoelectronics", IEST, Shibpur, 29-31 March, 2018.
4. Invited speaker *School on Electronic Transport and Magnetism: From Fundamentals to Applications-2016*, Talk: "Atomistic simulation of low dimensional systems for quantum transport", held at **Harishchandra Research Institute (HRI), Allahabad, India, 22 February - 2nd March , 2016.**
5. Invited speaker at **CEFIPRA Indo-French Workshop on Emerging Trends in Electron Device Modeling**, Talk: "Study of next generation 2D and 1D channel material MOSFETs" held at **Indian Institute of Science, Bangalore, 30 March -1 April, 2015.**
6. Invited speaker **CeCAM Workshop on High performance models for charge transport in large scale materials systems**, Talk: "Study of next generation 2-D channel material MOSFETs with empirical tight binding – NEGF formalism", held at **Bremen Center for Computational Materials Science (BCCMS), Universität Bremen, Germany, 5-10 October, 2014.**
7. Invited speaker at the *INUP Compact modeling workshop*, held at **Indian Institute of Science, Bangalore, 22 - 23 August, 2014.**
8. Invited technical talk "2-Dimensional Channel Materials based Next Generation Nano-scale MOS devices", at **National Institute of Technology, Sikkim, 05 March, 2014.**
9. **Resource Person: Faculty Development Program on Micro and Nanoelectronics, sponsored by VTU and VGST**, held at Guru Nanak Dev Engg. College(GNDEC), **Bidar, Karnataka, 6<sup>th</sup> -9<sup>th</sup> August 2013.**
10. Invited technical talk "Exploration of Two Dimensional Channel Materials for Next Generation CMOS Technology" and "Current Trends in MOS non-volatile memories: Nanoscale Engineering and Emerging Devices", organized by **IEEE Electron Devices Society , Kolkata Chapter**, held at **Jadavpur University, 23 Nov. 2012.**

#### Session chair/ Conference committee/ other advisory roles

- **Session chair, Surface science division, session O4, DPG Spring meeting -2018, 11-16 March, 2018, Berlin, Germany.**
- TPC member, **IEEE Nanotechnology Materials and Devices Conference (NMDC) – 2018, Portland, USA & IEEE Nanotechnology Materials and Devices Conference (NMDC) – 2020, Nanjing, PRC.**
- Session Chair, IEEE 2021 Devices for Integrated Circuit (DevIC 2021), KGEC, Kolkata, 19-20 May, 2021.
- Workshop Chair, and Session Chair, 1st IEEE International Symposium on Devices, Circuits and Systems (ISDCS 2018), IEST, Shibpur, 29-31 March, 2018.
- Tutorial Chair 2018 IEEE Electron Device Kolkata Conference (2018 IEEE EDKCON), Kolkata, 17-18 Nov, 2018.